

AC 2009-417: LABORATORY IMPLEMENTATION OF A SMALL-SCALE CAN-BASED PM BLDC MOTOR CONTROL FOR AUTOMOTIVE ACCESSORY ELECTRIFICATION

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Title of the Paper:
**Laboratory Implementation of a Small-Scale CAN-Based
PM BLDC Motor Control for Automotive Accessory Electrification**

Abstract

This paper presents laboratory development of a real-time controller for small Permanent Magnetic Brushless Direct Current (PM BLDC) motor using a Controller Area Network (CAN) communication scheme. The CAN communication bus transmits and receives information between modules to control the speed, acceleration/deceleration, and rotating direction of the motor. The laboratory consists of five major hardware: single chip microcontroller, three module boards, PM BLDC motor, logic-input quad driver, and a power logic level gate driver. Microcontroller software is developed for eight major functions: controller initialization, service interrupt, switch, display, power converter, CAN communication, pulse width modulation control, and actual motor speed measurement. The motivation of this work is to enhance the students' learning of the PM BLDC motor control and CAN system in a laboratory setting. This work is important because electric drivetrain, accessory electrification, and the CAN communication system are widely employed in the electric and hybrid electric vehicles.

Introduction

Electric drivetrain and electrically powered automotive subsystems are at the heart of the Electric Vehicle (EV) and Hybrid Electric Vehicle (HEV). The traction motor in the electric drivetrain is a major propulsion component for vehicle powertrain hybridization. Electrically powered automotive subsystems, such as hydraulic power steering, engine cooling system, etc., is essential to the EV and HEV because the EV has no engine and the HEV frequently has the engine turned off to conserve energy. For an instance, an electric air conditioning compressor (powered by the electric motor/battery instead of mechanical engine belts) allows air conditioning to function even when the engine shuts off during vehicle stops. Other examples of such applications include water pump, oil pump, fuel pump, air compressor, variable valve actuator, power steering pump, and many more. Motor control is important to deliver traction and auxiliary functions for the vehicles.

Electrically powered automotive accessories, instead of mechanically powered ones, allow the accessories to operate independently of the engine so they can perform at the precise speed, pressure, or flow rate required. Accessory electrification provides the following benefits: fuel savings, thermal load reductions, emission reductions, significant flexibility in placing components inside the engine compartment, and a great potential for enhancements¹⁻⁴. Either using a traction motor to propel the vehicle or an electric motor (replacing the conventional belt-driven hydraulic pump/compressor) to delivery auxiliary functions requires a motor driver or a motor controller. Additionally, there are a large number of electric motors inside the conventional vehicle and this number is growing as more options are being incorporated⁵. Some examples of use of motors inside the vehicle are window rolling functions, wiper control, seat and mirror movements and others. Controllers are also needed for these motors.

Differing from the industrial applications of motors, the motors used in electric drivetrain and accessories usually require frequent stops and starts, high rates of acceleration/deceleration, high torque and low-speed hill climbing, low torque and high-speed cruising, and a very wide speed range of operation. Recent advancements in permanent magnetic materials and motor design have made the Permanent Magnetic Brushless DC (PM BLDC) motor a great candidate for automotive electric traction and accessory electrification applications⁶⁻¹¹. A typical motor control consists of electric motor, power converter, electronic controller and information network such as Controller Area Network (CAN). The power converter supplies the proper voltage and current to the electric motors. The electronic controller manages the command signals to power converter and then controls the operation of the electric motor to produce required torque and speed. These command signals and information are transmitted through CAN bus.

CAN is a serial bus communications protocol initially created by German automotive system supplier Robert Bosch in the mid-1980s for automotive applications as a method for enabling robust serial communication¹². CAN defines a standard for efficient and reliable communication between sensor, actuator, controller, and other nodes in real-time applications and its goal is to make automobiles more reliable, safe and fuel-efficient while decreasing wiring harness weight and complexity. Since its inception, the CAN protocol has gained widespread popularity in automotive applications¹³⁻¹⁵. The unique feature of the CAN protocol is the no need of origination and destination addresses for a message. Instead, an identifier is embedded in each message, which means that a node can be connected to the network without altering any existing software or hardware on the network. It also means that multiple nodes can act upon the same message, allowing multicasting capabilities. As shown in Fig. 1, a number of electronic control units used to control engine, transmission, drive information, antilock brake system, door main and other controllers are typically connected together to form a CAN bus.

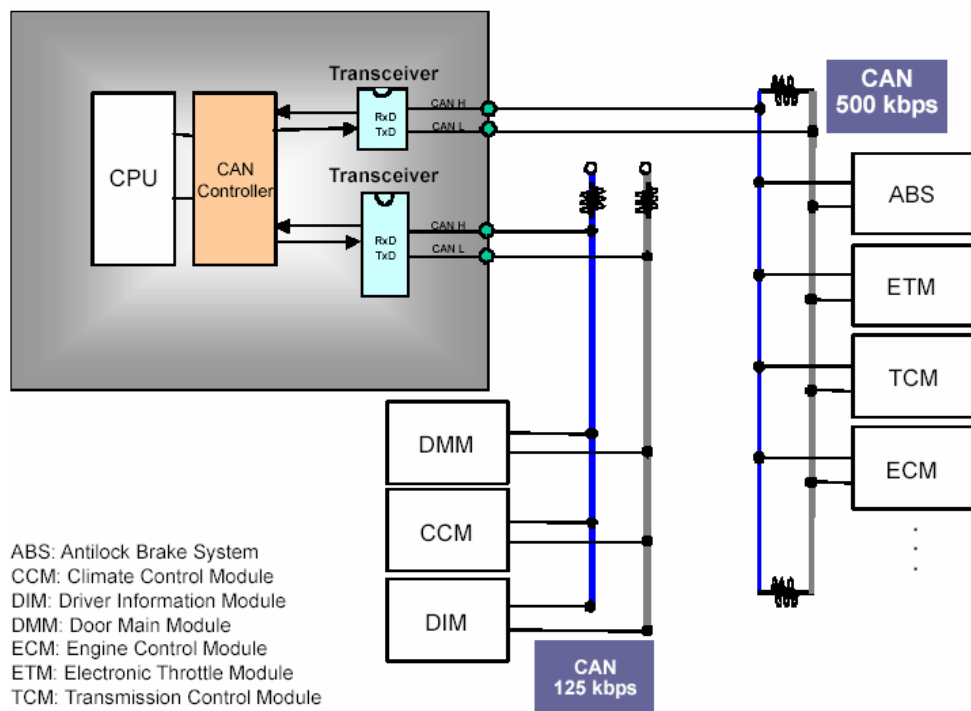


Figure 1. An example of CAN connection diagram in the vehicle

This paper presents a laboratory development of a real-time controller for a small PM BLDC motor through a network system. The network system uses a CAN communication bus to transmit/receive information between modules to control the speed, acceleration/deceleration, and rotational direction of the motor. This work is important as electric drivetrains, accessory electrification, and CAN communication systems are employed in the electric and the hybrid electric vehicles. The system consists of five major hardware: Renesas single chip microcontroller M16C29 MCU, three module boards, PM BLDC motor, TelCom TC4469 logic input Complementary Metal–Oxide–Semiconductor (CMOS) quad driver, and International Rectifier power Metal–Oxide–Semiconductor Field Effect Transistor (MOSFET) logic level gate driver. A Renesas High-Performance Embedded Workshop (HEW) is used to develop system software to perform eight major functions: MCU initialization, interrupt generation, switch, Liquid Crystal Display (LCD), power converter, CAN communication, Pulse Width Modulation (PWM) control, and motor real speed measurement. The motivation of this work is to establish laboratory and project oriented learning environment which encourages students to apply and absorb knowledge gained in lecture in a hands-on environment. This work would be useful in controlling various motors in modern vehicles.

Hardware Apparatus

Figure 2 shows the developed platform which uses a Renesas single chip microcomputer M16C29 MCU, three module boards, BLY17 PM BLDC motor, TelCom TC4469 logic input complementary CMOS quad driver, International Rectifier power MOSFET logic level gate driver, and communicates through a CAN bus. The control block diagram for motor is also shown in Fig. 2.

A brushless motor is constructed with a permanent magnet rotor and wire wound stator poles. Electrical energy is converted to mechanical energy by the magnetic attractive forces between the permanent magnet rotor and a rotating magnetic field induced in the wound stator poles. The generated stator flux interacts with the rotor flux which is generated by a rotor magnet. This flux interaction defines the torque and speed of the motor. In order to generate the maximum torque, the angle between the stator flux and rotor flux is kept close to 90° . Additionally, the voltage strokes must be properly applied to the two phases of the three-phase winding system. Due to these phenomena, the motor requires electronic control for proper operation.

To rotate a BLDC motor, the stator windings should be energized in a sequence. It is important to obtain the rotor position in order to determine which winding will be energized following the energizing sequence. Most BLDC motors have three Hall-effect position sensors embedded into the stator on the non-driving end of the motor¹⁶. The three sensors are offset from each other by 60 electrical degrees so that each sensor output aligns with one of the electromagnetic circuits. Each sensor element generates a digital signal output at a high level of 180 electrical degrees in electrical rotation, and a low level of the other 180 electrical degrees. Based on the combination of these three Hall-effect sensor signals, the exact sequence of commutation can be determined. This project used an Anaheim Automation BLY17 PM BLDC motor¹⁷.

The M16C29 is a single-chip microcontroller, as shown in Fig. 3¹⁸, developed for built-in applications and supporting instructions written in C language. The instruction set consists of 91

discrete instructions that allows performing register-to-register, register-to-memory, and memory-to-memory operations, as well as arithmetic/logic operations on bits and 4-bit data¹⁹. Each microcontroller contains one CAN module which can transmit and receive messages in both standard 11-bit and extended 29-bit formats, and is suitable for control in automotive applications. The M16C29 microcontrollers are widely used in automotive body, safety and audio.

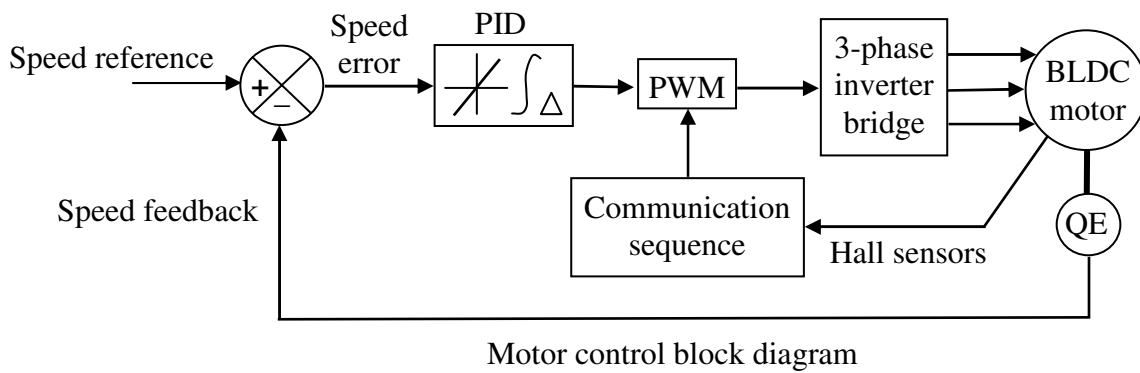
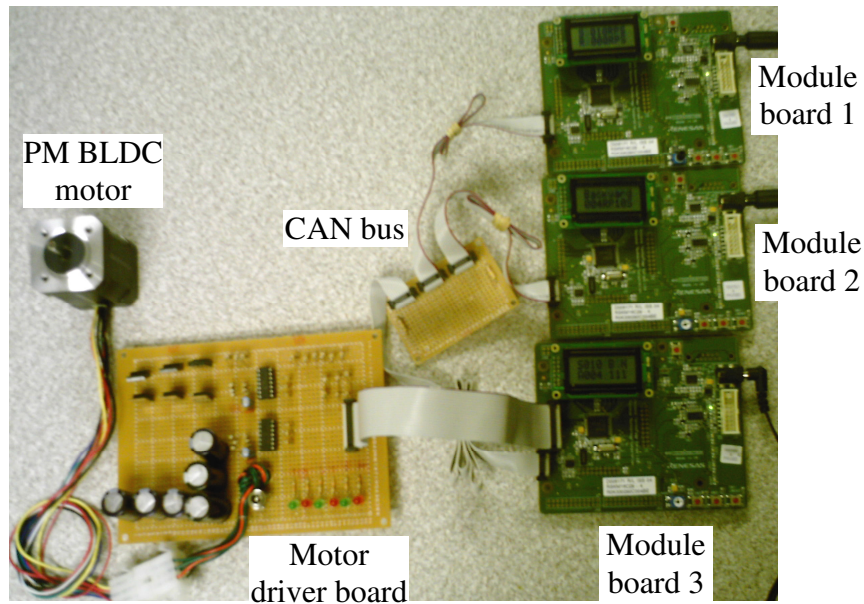


Figure 2. Developed system for PMBL DC motor control

This project used three Renesas Starter Kit (RSK)¹⁸ module boards. The RSK features include microcontroller programming, user code debugging and user designed circuitry such as switches, LCD and potentiometer. The purpose of these boards is to evaluate the capability of the motor and its peripherals. Figure 4 shows a typical RSK board and its setups for three module boards in this project. Each board has three switches and two of them are used to turn a motor on/off and change the motor rotational direction. A single turn potentiometer is connected to the microcontroller. The LCD in each board is used to display motor setting speed, acceleration, rotational direction, and start/stop status. The LCD also displays CAN bus communication between modules.

Board-1

The functions of board-1 are setting motor speed, receiving motor actual speed and displaying both speeds. The LCD displays the motor setting speed by reading potentiometer Analog-to-Digital (A/D) converter data and displays the actual speed received by CAN bus from board-3.

Board-2

The major functions of board-2 are setting motor rotational directions and acceleration/deceleration.

Board-3

The major function of board-3 is submitting motor control signals to the motor driver board. The LCD on board-3 displays the setting speed by receiving data from board-1, the setting acceleration/deceleration by receiving data from board-2, the motor rotational direction (forward or backward) by receiving data from board-2, the motor start/stop status by pressing SW1 on board-3, and the Proportional-Integral-Derivative (PID) controller parameter by pressing SW2 on board-3. This information is transmitted between boards via CAN bus.

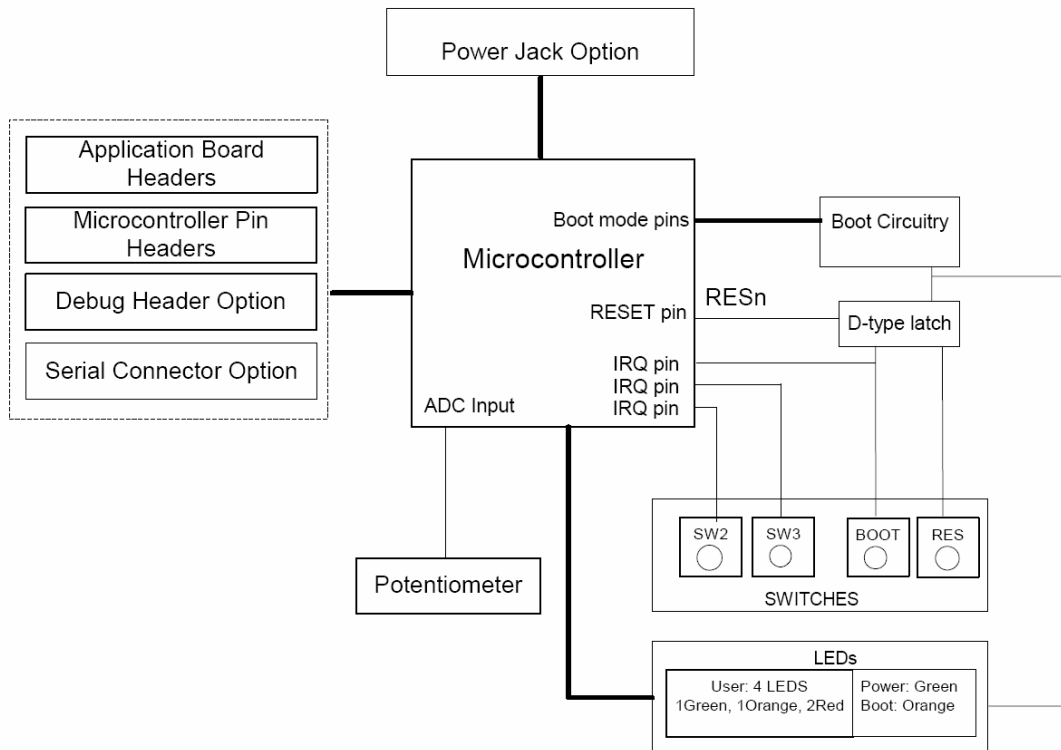


Figure 3. Block diagram of M16C29 CPU board components and their connectivity¹⁸

The output stage of motor controller is made up of transistors. The transistor in this project is TC4469 device which is a four-output CMOS buffer/MOSFET driver with 1.2 Amp peak drive capability²⁰. The inputs to the transistor are configured as logic gates. The driver can continuously source up to 250 mA into ground reference loads. Therefore, it is ideal for direct driving low current motors or driving MOSFETs in an H-bridge configuration for higher current motor drive.

The Gate driver is a power amplifier that accepts low-power input from a controller and produces the appropriate high-current gate drive for a power MOSFET. A gate driver is used when a PWM controller cannot provide the required output current to drive the gate capacitance in an associated MOSFET. Partitioning the gate-drive function off the PWM controller allows the controller to run in a cooler condition, and be more stable, by eliminating the high peak currents and heat dissipation of driving a power MOSFET at very high frequencies. In this project, IRL13705N and IRF5305 make a 3-phase-bridge to drive the 3-phase BLDC motor.

The motor speed is controlled in a closed loop by measuring the actual speed of the motor. The error between the set speed and actual speed is calculated. A PID controller is used to amplify the speed error and dynamically adjust the PWM duty cycle.

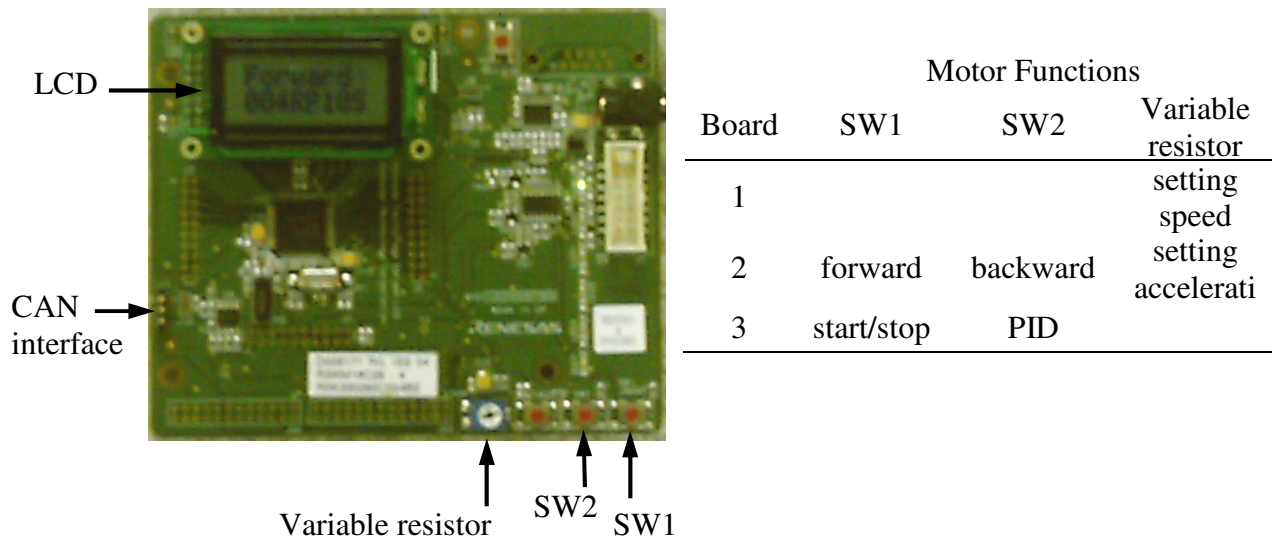


Figure 4. Setups of three RSK module boards

The CAN communication protocol is a carrier-sense multiple-access protocol with collision detection and arbitration on message priority. CAN protocol includes CAN layers and CAN messages frames. The first version of the CAN protocol, Standard CAN 2.0A, is for applications up to 125 kbps with a standard 11-bit identifier. The version of CAN 2.0B extends to a 29-bit identifier. Figure 5 shows the CAN 2.0 protocol layers for A and B. The CAN module for the M16C29 microcomputer is a communication controller implemented with a CAN 2.0B protocol. CAN 2.0B has application, data link and physical layers. The application layer generates or interprets data and actually sends and receives messages. The object layer is responsible for handling messages, such as selecting a transmitted or received message, working as an interface between the transfer layer and the application program running on the CPU. The transfer layer ensures that messages adhere to the protocol. The physical layer defines the physical (hardware) implementation and the electrical (signal level) implementation of the bus, network cabling, connector type, pin-out, physical data rates, maximum transmission distances, and data transmission encoding. In CAN 2.0B, the data link layer transmits frames over the network. Different network and protocol characteristics are defined by different data link layer specifications.

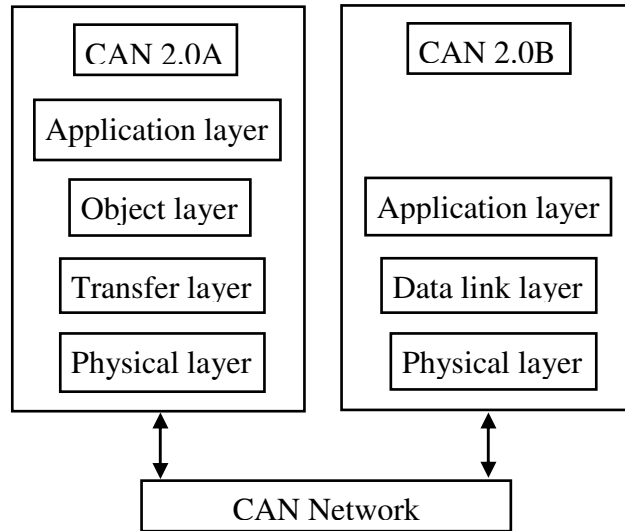


Figure 5. CAN 2.0 protocol layers for A and B

In a CAN system, data is transmitted and received using message frames. Message frames carry data from a transmitting node to one, or more receiving nodes. Four different message frames exist on CAN network bus: data frame, remote frame, error frame, and overload frame. The data frame contains seven sub fields: start, arbitration, control, data, Cyclical Redundancy Check (CRC), Acknowledge (ACK), and end of frame. The start field is made up of a single dominant bit which is used by receiving nodes to synchronize the receipt of a data frame. The arbitration field contains the identifier number of a message. The identifier number is used by receiving nodes to determine the acceptance or rejection of a particular data frame. The identifier contains either 11 bits or 29 bits for standard and extended format respectively. The arbitration field also contains a Remote Transmission Request (RTR) bit that is used to distinguish a data frame from a remote frame. The control field contains four bits that specify the data length in bytes. The data length is specified by four separate bits, allowing data lengths from one to eight bytes. The data field contains the actual transfer message. For each byte, the most significant bit is transmitted first. The CRC field is used to test the validity of the data received. The CRC ends with a CRC delimiter bit. The ACK field contains an ACK slot and an ACK delimiter. The ACK slot is used by a receiving node to inform the transmitter that a message (data frame) has been accepted correctly by sending one dominant bit within the ACK slot. The ACK delimiter bit is a single recessive bit. The last field, the end of frame field, consists of seven recessive bits.

TimerS in the M16C29 microcontroller is a multifunctional input/output port for time measurement and waveform generation. A TimerS value is measured 6 times in one electrical cycle on every transition from Hall-effect sensor. This electrical cycle repeats as many times as the number of rotor pole pairs to complete a mechanical rotation. For example, if the rotor has two pole pairs, the electrical cycle repeats twice for one mechanical rotation of the shaft. The TimerS value is averaged over one rotation and the averaged value is used to determine the motor actual speed. Translating the TimerS value into motor speed depends upon three factors: operating frequency, TimerS prescale and number of rotor pole pairs. The motor actual speed, setting speed and trace speed is calculated by Eq. (1), (2) and (3) respectively.

$$\text{Actual speed in RPS} = \text{operating frequency} / (\text{TimerS count} \times \text{TimerS prescale} \times \text{Number of pole pairs} \times 6) \quad (1)$$

$$\text{Setting speed} = \text{rated motor speed} \times \text{Analog-to-Digital Converter (ADC) value} / \text{maximum ADC value} \quad (2)$$

where ADC converts analog input to a digital value. The ADC also introduces errors due to finite intervals in time between samples and finite number of binary output values.

$$\text{Trace Speed} = \text{Setting Acceleration} \times \text{Counter (10ms unit)} / 10000 \text{ (10ms unit)} \quad (3)$$

Motor speed error is the difference between the trace speed and actual speed values. This is the error in terms of a number of encoder counts per unit time. The error may be positive or negative, indicating the speed is higher or lower than the set reference. The error is amplified through a PID algorithm which adjusts the gain to the PWM based on several characteristics of the feedback, not just the current value. The amplified error is used to readjust the PWM duty cycles. Figure 6 shows the computation of PWM where a new PWM value is calculated by adding the PID error to the old PWM. The PID error is the summation of P_term_error, I_term_error, and D_term_error which are defined in Eq. (5), (7) and (9) respectively.

$$\text{Error_term} = \text{trace speed} - \text{actual speed} \quad (4)$$

$$\text{P_term_error} = K_p * \text{Error_term} \quad (5)$$

$$\text{I_state} = \text{I_state} + \text{Error_term} \quad (6)$$

$$\text{I_term_error} = K_i * \text{I_state} \quad (7)$$

$$\text{D_state} = \text{Error_term} \quad (8)$$

$$\text{D_term_error} = K_d * (\text{Error_term} - \text{D_state}) \quad (9)$$

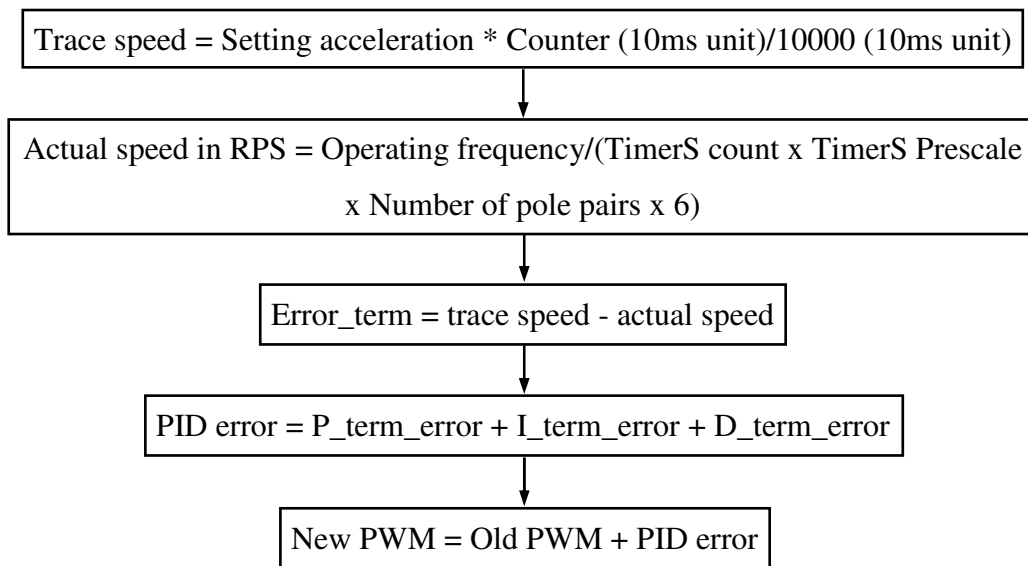


Figure 6. Flowchart of PWM computation

Proportional coefficient K_p multiplied by the error term provides a linear response. Integral coefficient K_i multiplied by the error term and added to the sum of all previous integral terms determines an I_term_error. The I_term_error responds to the change of error from one PID cycle to the next. Derivative coefficient K_d multiplied by the difference between the previous error and the current error determines a D_term_error.

Software Implementation

HEW is used to develop the control software for eight major functions: (a) MCU initialization, (b) service interrupt generation, (c) switch, (d) LCD display, (e) A/D converter, (f) CAN communication, (g) PWM control and (h) measuring motor real speed. HEW is an integrated development tool that allows the user to write, compile, program and debug software on M16C29 microcontroller. The software developed in this project can successfully perform CAN communication and control PM BLDC motor. The code memory size is 2773 bytes and the RAM size is 733 bytes. Figure 7 displays the system summary of the developed program in the HEW environment.

Address(size)	Section	Address(size)	Section	Label:
000000(000400)>>		000000(000400)		
000400(0000F0)	[D] bss_NE	000400(0000F0)	[D] bss_NE	[G] 000400:_ad_data
0004F0(000001)	[D] data_NO			[G] 0004c8:_asc_d
0004F1(000042)	[D] bss_NO			[G] 0004cc:_real_speed
000533(000600)	[D] stack			[G] 0004d6:_pulse_widt
000b33(000001)				[G] 0004d8:_pulse_flag
000b34(00F4cc)				[G] 0004da:_new_tr
010000(0e8000)				[G] 0004dc:_old_tr
0F8000(000010)	[R] rom_FE			[G] 0004de:_acc_counte
0F8010(00002d)	[R] rom_FD			[G] 0004e0:_acc_counte
0F803d(000001)	[R] data_NOI			[G] 0004e2:_error_spee
0F803e(002773)	[C] program			[G] 0004e4:_last_error
0Fa7b1(0000F1)	[C] interrupt			[G] 0004e6:_error_pid
0Fa8a2(00545e)				[G] 0004e8:_error_spee
0FFd00(000100)	[R] vector			[G] 0004ea:_error_spee
0FFE00(0001Fc)				[G] 0004ec:_new_pwm
0FFFFc(000004)	[R] fvector			[G] 0004ee:_old_pwm
0FFFFFF				
		0004F0(000001)	[D] data_NO	[G] 0004f1:_rec_data
		0004F1(000042)	[D] bss_NO	[G] 000512:_F_RCV_DATA
				[G] 000513:_last_rcv_d
				[G] 000514:_last_rcv_d
				[G] 000515:_last_rcv_d
				[G] 000516:_rcv_flag
				[G] 000517:_last_ad_da

Figure 7. System summary of the developed program in the HEW environment

Operation Procedures

The operation procedures for the developed platform are described here.

- (1) Plug in the power supplies for module board-1, 2, 3 and motor driver.
- (2) Select the motor rotational direction (forward or backward) by pressing SW1 (forward) or SW2 (backward) on module board-2.
- (3) Select the motor setting speed by adjusting variable resistor on module board-1.
- (4) Select the motor setting acceleration/deceleration by adjusting variable resistor on module board-2.
- (5) Select the PID parameter by pressing SW2 on module board-3.

- (6) Start the motor by pressing SW1 on module board-3. The acceleration/deceleration setting determines the motor running speed (speed up or down). The motor finally reaches the setting speed.
- (7) Change motor direction by pressing SW1 to forward and pressing SW2 to backward on module board-2 when motor is running.
- (8) Change motor acceleration/deceleration by adjusting variable resistor on module board-2 when motor is running.
- (9) Change motor setting speed by adjusting variable resistor on module board-1 when the motor is running.
- (10) Stop the motor by pressing SW1 again on module board-3.

The developed system drives a pole-pair motor running at 2500 rpm. The major contributor to the CPU load is PWM generation which is updated in the 16-bit timer overflow service and frequency is 9.77 KHz. Hall sensor signal measurement is performed in the input of the PWM TimerS service routine. The baud rate of CAN communication is 500 kbps. Considering a pole-pair motor runs at 2500 rpm and the incoming Hall sensor signal frequency is 250Hz, board-3 (three identifiers each for transmitting and receiving) must transmit real motor speed to CAN bus every 4 ms. An example of the trajectory of the controlled motor speed was shown in Figure 8.

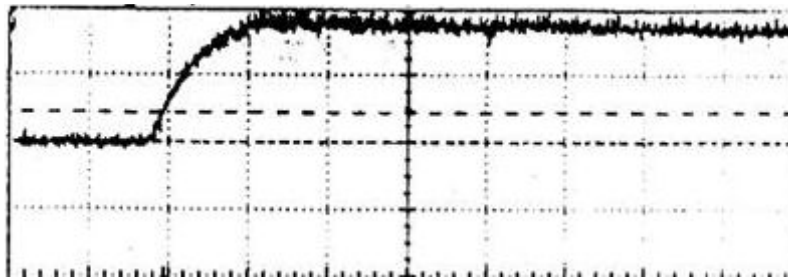


Figure 8. An example of the controlled motor speed plot

Results and Discussions

Today, there is a strong trend in the automotive industry to replace mechanical and hydraulic loads in vehicles with their electric counterparts. This is due to advantages which include an increasing in efficiency as well as comfort and safety related benefits. Electric motors play an important role to deliver auxiliary functions in electrified automotive accessories. Not to mention the electric traction motor is a major propulsion component in the EV and HEV. As PM BLDC motors become widely implemented for automotive accessory electrification and electric drive applications, there is a need to train and educate students in the motor controller.

A laboratory is constructed for real-time controller of small PM BLDC motor using CAN communication bus. The motivation of this work is to establish laboratory and project oriented learning environment which encourages students to apply and absorb knowledge gained in lecture in a hands-on environment. The laboratory experiment simulates various motor operations for vehicle applications for accessory and traction by control motor speed, acceleration, rotational direction and start/stop status. The developed hardware and software has met the major functions of the PM BLDC motor in a CAN communication environment.

This paper describes the development of the laboratory experiment for educational purpose. The developed laboratory experiment will be implemented in two courses: Electric Machines and Power Electronics, and Hybrid Electric Vehicle Technology. The work is still ongoing to layout the laboratory exercises for students. It is planned to implement this laboratory in the Fall term 2009.

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